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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/560,105	04/28/2000	Ulf Ahlfors	3964-10 (6563-50462)	4173

7590 09/11/2003

Coudert Brothers  
600 Beach Street  
San Francisco, CA 94109

EXAMINER

FOX, JAMAL A

ART UNIT

PAPER NUMBER

2664

DATE MAILED: 09/11/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

09/560,105

Applicant(s)

AHLFORS ET AL.

Examiner

Jamal A Fox

Art Unit

2664

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 28 April 2000.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-52 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-5, 11, 27-31 and 37 is/are rejected.
- 7) ☒ Claim(s) 6-10, 12-26, 32-36 and 38-52 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 28 April 2000 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.  
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☐ All b) ☐ Some \* c) ☐ None of:  
1. ☐ Certified copies of the priority documents have been received.  
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).  
\* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).  
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 2 & 5. 6) ☐ Other: \_\_\_\_\_

**DETAILED ACTION**

***Claim Rejections - 35 USC § 102***

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

2. Claims 1-5, 11, 27-31 and 37 are rejected under 35 U.S.C. 102(a) as being anticipated by Kadambi et al. U.S. Patent No. 6,335,932.

Referring to claim 1, Kadambi et al. discloses a method of managing packets queues (col. 3 lines 10-22, here it is understood that the memory management unit manages packet queues) in a switch (col. 2 line 61- col. 3 line1) having a limited primary memory (col. 3 lines 17-21, 35-40, 60-64, and col. 4 lines 1-13, here it is understood that the internal memory is the primary memory) including a number of queues (col. 3 lines 14-19 and 30-42, here it is understood that the channels are the queues) for switching data packets between input ports (Fig. 14 ref. Sign 24) and output ports (col. 17 lines 55-60), and connected to a larger secondary memory (col. 3 line 11-21, 35-41, 50-62 and col. 4 lines 4-25, here it is understood that the secondary memory is the external memory) also including a number of queues, comprising the steps of dividing a data stream incoming on the input ports (Fig. 14 ref. Sign 24) intended for respective output ports (col. 17 lines 55-60) into two parts, of which the first part contain flows to be sent to an output port queue of the primary memory and the second part contain flows to be sent to the secondary memory (col. 3 line 45-64 and col. 4 lines 10-31).

Referring to claim 2, Kadambi et al. discloses the method according to claim 1, wherein the data of the second part is stored in a third memory (Fig. 2 ref. Sign 70 and col. 5 lines 64-65) before it is sent to the secondary memory (Fig. 1 ref. Sign 12).

Referring to claim 3, Kadambi et al. discloses the method according to claim 2, wherein the primary memory is a fast memory internal (col. 3 lines 8-10) on a chip and the secondary memory is external (col. 3 lines 18-22) from the chip.

Referring to claim 4, Kadambi et al. discloses the method according to claim 3, wherein the third memory is provided as store queues forming part of the primary memory (col. 35 lines 30-37).

Referring to claim 5, Kadambi et al. discloses the method according to claim 1, wherein the data of the incoming data stream is identified as belonging to flow groups, each flow group containing a number of flows (Fig. 19 and respective portions of the specification, here it is understood that a trunk group is a flow group).

Referring to claim 11, Kadambi et al. discloses the method according to claim 5, wherein the data packets of the incoming data stream have a priority value and are identified as belonging to priority groups and the flow groups are formed by means of priority (col. 20 line 54, col. 32 lines 3-12, col. 32 lines 23-43, here it is understood that the priority values reside in the priority queues).

Referring to claim 27, Kadambi et al. discloses an arrangement for managing packet queues (col. 3 lines 10-22, here it is understood that the memory management unit manages packet queues) in a switch (col. 2 line 61- col. 3 line1) having a limited primary memory (col. 3 lines 17-21, 35-40, 60-64, and col. 4 lines 1-13, here it is

understood that the internal memory is the primary memory) including a number of queues (col. 3 lines 14-19 and 30-42, here it is understood that the channels are the queues) for switching data packets between input ports (Fig. 14 ref. Sign 24), and connected to a larger secondary memory (col. 3 line 11-21, 35-41, 50-62 and col. 4 lines 4-25, here it is understood that the secondary memory is the external memory) also including a number of queues, comprising: means for dividing a data stream incoming on the input ports (Fig. 14 ref. Sign 24) intended for respective output ports (col. 17 lines 55-60) into two parts, of which the first part contain flows to be sent to an output port queue of the primary memory and the second part contain flows to be sent to the secondary memory (col. 3 line 45-64 and col. 4 lines 10-31).

Referring to claim 28, Kadambi et al. discloses the arrangement according to claim 27, wherein the data of the second part is stored in a third memory (Fig. 2 ref. Sign 70 and col. 5 lines 64-65) before it is sent to the secondary memory (Fig. 1 ref. Sign 12).

Referring to claim 29, Kadambi et al. discloses the arrangement according to claim 28, wherein the primary memory is a fast memory internal (col. 3 lines 8-10) on a chip and the secondary memory is external (col. 3 lines 18-22) from the chip.

Referring to claim 30, Kadambi et al. discloses the arrangement according to claim 29, wherein the third memory is provided as store queues forming part of the primary memory (col. 35 lines 30-37).

Referring to claim 31, Kadambi et al. discloses the arrangement according to claim 27, wherein the data of the incoming data stream is identified as belonging to flow

groups, each flow group containing a number of flows (Fig. 19 and respective portions of the specification, here it is understood that a trunk group is a flow group).

Referring to claim 37, Kadambi et al. discloses the arrangement according to claim 31, wherein the data packets of the incoming data stream have a priority value and are identified as belonging to priority groups and the flow groups are formed by means of priority (col. 20 line 54, col. 32 lines 3-12, col. 32 lines 23-43, here it is understood that the priority values reside in the priority queues).

***Allowable Subject Matter***

3. Claims 6-10, 12-26, 32-36, 38-52 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

***Conclusion***

**4. Any response to this action should be mailed to:**

Commissioner of Patents and Trademarks  
Washington, D.C. 20231

**or faxed to:**

(703) 305-3988, (for formal communications intended for entry)

**Or:**

(703) 305-3988 (for informal or draft communications, please label  
"PROPOSED" or "DRAFT")

Hand-delivered responses should be brought to Crystal Park II, 2121  
Crystal Drive, Arlington, VA. 22202, Sixth Floor (Receptionist).

**5. Any inquiry concerning this communication or earlier communications from the  
examiner should be directed to Jamal A. Fox whose telephone number is (703) 305-  
5741. The examiner can normally be reached on Monday-Friday 6:30 AM - 5:00 PM.**

If attempts to reach the examiner by telephone are unsuccessful, the examiner's  
supervisor, Wellington Chin can be reached on (703) 305-4366. The fax phone  
numbers for the organization where this application or proceeding is assigned are (703)  
872-9314 for regular communications and (703) 872-9315 for After Final  
communications.

Any inquiry of a general nature or relating to the status of this application or  
proceeding should be directed to the receptionist whose telephone number is (703) 306-  
0377.

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J.A.F.  
Jamal A. Fox

A handwritten signature in black ink, appearing to be 'J.A.F.', located on the right side of the page.